

# DSO138mini Oscilloscope DIY Kit (Model: 13805K)

## User Manual

Rev. 04

Applicable PCB: Main: 109-13800-001  
Analog: 109-13801-00H  
Applicable firmware: 113-13810-110 or later

### Tools you need

- ① Iron (20W)
- ④ Screw driver
- ② Solder wire
- ⑤ Flush cutter
- ③ Multimeter
- ⑥ Tweezers

### Before you start

- ① Check values & quantities against parts listed
- ② Understand all part polarities and orientations
- ③ Prepare a USB cable with USB-micro connector

### Notes

- ① Instructions for optional parts (including *BNC probe, enclosure, and battery charger*) are not given in this manual. If you have purchased these parts please refer to their own manuals available at [www.jyetechnology.com](http://www.jyetechnology.com).
- ② Please visit [www.jyetechnology.com](http://www.jyetechnology.com) for other documents about schematics, troubleshooting, firmware upgrade, mechanical, waveform upload, etc.

## Step 2 Assembly Analog Board (follow the order as numbered)

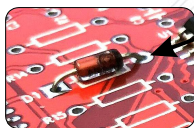
### 1. Resistors



**Note:**  
Always meter resistor values before soldering because color bands are easy to mis-read.  
Resistors are all 1/8W.

- R1, R13 : 100K  $\Omega$
- R2 : 1.8M  $\Omega$
- R3, R15 : 200K  $\Omega$
- R4 : 2M  $\Omega$
- R5 : 20K  $\Omega$
- R6, R14, R17 : 300  $\Omega$
- R7, R11 : 180  $\Omega$
- R8, R12 : 120  $\Omega$
- R9, R10, R16 : 1.1K  $\Omega$

### 2. Diode



**Cathode**

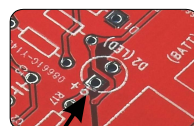
□ D1 : Zener, 2.0V

### 3. HF-Chokes

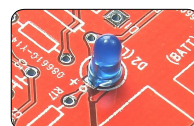


□ L1, L2 : 100  $\mu$  H

### 4. LED

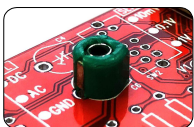


Solder positive pole (the longer lead) to the square pad



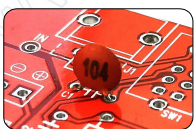
□ D2 : LED, blue, dia. 3mm

### 5. Capacitor trimmers



□ C4, C6 : 5 - 30pF

### 6. Ceramic Capacitors

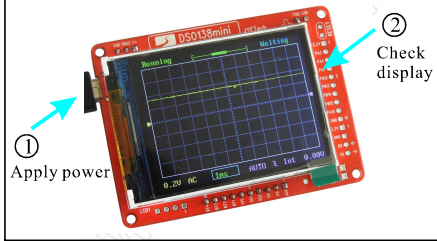


- C1 : 0.1  $\mu$  F
- C2 : 220pF
- C3 : 3pF
- C5 : 1pF
- C7 : 120pF

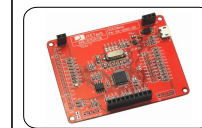
## Step 1 Test and Assembly Main Board

### 1. Check the main board

- ① Before mounting any parts to the main board Use an USB cable with USB-Micro plug to power the main board through J7.
- ② You should see the scope boots up to a screen similar to the photo below. D1 (LED) should blink three times during the booting.



### 2. Pin-headers (female)

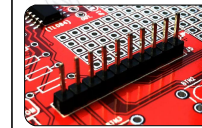


- J4 : 1 X 10 pin
- J8 : 1 X 2 pin
- J9 : 1 X 3 pin

### Attention

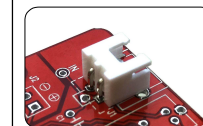
**Do not solder any parts to the board if you find problem. Otherwise warranty will be voided. Report to your vender or JYE Tech for any problem found.**

### 10. Pin-headers (male)



- J5 : 1 X 10 pin
- J2 : 1 X 3 pin
- J3 : 1 X 2 pin

### 7. Pin header



- J1, J6 : 2 Pin, 2.54mm, rightangled

**Note:**  
Do not install J1 if BNC connector is to be used.

### 8. Electrolytic capacitors

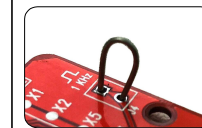


Solder positive pole (the longer lead) to the square pad



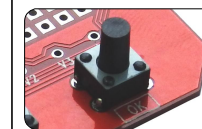
- C10, C11, C12, C13, C14 : 100  $\mu$  F / 16V

### 11. Test signal ring



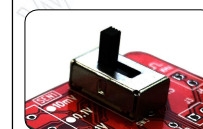
- 1) Make a small ring with a lead cut-off.
- 2) Solder the ring to the two holes of J4 (as shown in the photo).

### 12. Tact Switches



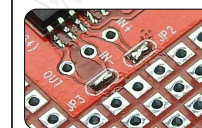
- BTN1, BTN2, BTN3, BTN4 : 6 X 6 X 9mm

### 9. Slide switches



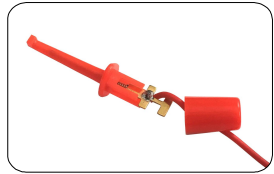
- SW1, SW2, SW3 : 2P3T
- SW4 : DPDT (side slide)

### 13. Jumpers



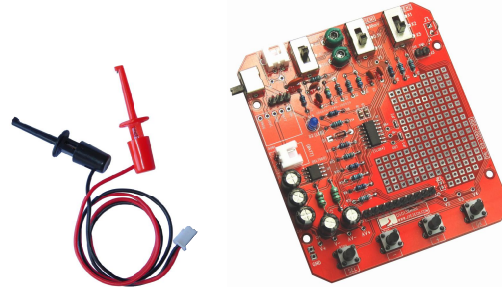
Short JP2 and JP3 with solder (see photo at left). Keep JP4, JP5, JP6 (on the bottom side) open. JP1 has been pre-shortened.

## 14. Hook Probes



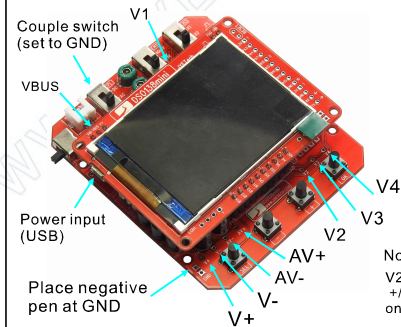
Put wire through hood cap and sold the wire onto hook terminal as shown. Match wire color with hook color.

## Finished look



## Step 3 Test analog board

### 1. Check voltages and controls

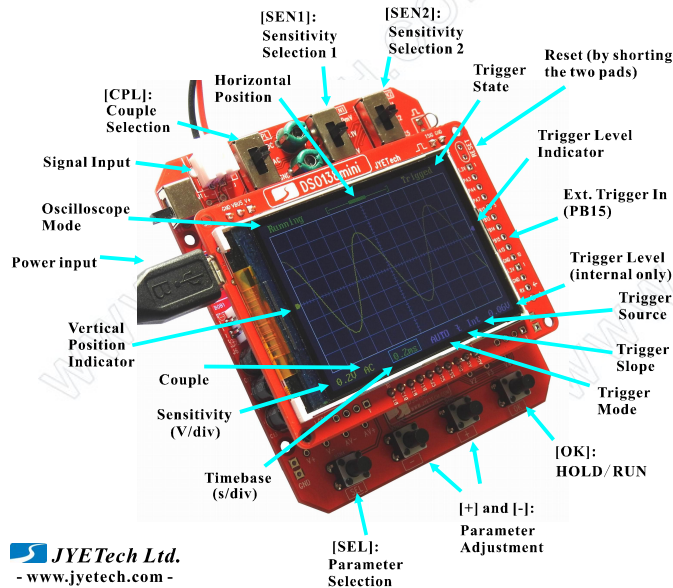


References (*) Input dependent	
VBUS	+5.10V
V+ (*)	> 3.5V
AV+ (*)	> 3.5V
V- (*)	< -3.5V
AV- (*)	< -3.5V
V1	0V
V2	1.05V
V3	2.1V
V4	-1.05V

Note:  
V2 - V4 could have up to +/- 0.2V variance depending on power supply voltage.

- Attach the main board to the analog board. Apply 5V DC power through J7.
- Set switch [CPL] to GND, [SEN1] to 1V, and [SEN2] to X5.
- Check voltages at the points as shown in the photo.
- Check slide switches and push-buttons for correct operation.
- Calibrate C4 & C6 if everything is fine (see instructions to the right).

### Display and Controls



### Attention

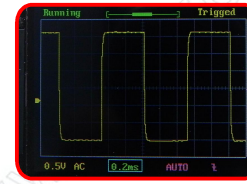
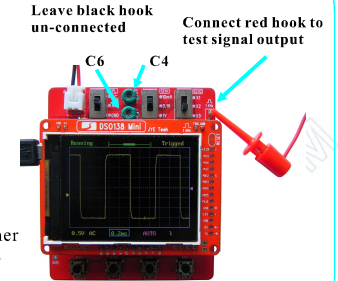
- Power supply voltage must not exceed 8V.
- Allowed maximum signal input voltage is 50Vpk (100Vpp).

### Specifications

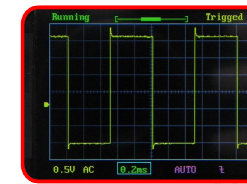
Max realtime sample rate	1MSa/s
Analog bandwidth	0 -- 200KHz
Sensitivity range	10mV/div - 5V/div
Max input voltage	50Vpk (1X probe)
Input impedance	1M ohm/20pF
Resolution	12 bits
Record length	1024 points
Timebase range	500s/Div -- 10us/Div
Trigger modes	Auto, Normal, and Single
Trigger position range	Center (fixed)
Trigger sources	Internal/External
Ext. trigger thresholds	Low: 1.1V, High: 2.2V
Ext. trigger input range	0V - 10V(max)
Power supply	3.5V - 5V DC
Current consumption	< 100mA
Dimension	85 x 75 x 15 (mm)
Weight	50 gram (without probe)

### Calibrating C4 & C6

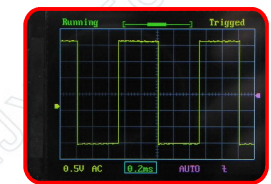
- Insert hook probe to J1. Connect the red hook to the test signal terminal J4 and leave the black hook un-connected.
- Set [SEN1] switch to 0.1V and [SEN2] switch to X5. Set [CPL] switch to AC or DC.
- Adjust timebase to 0.2ms. You should see waveform similar to that shown in photos below. If traces are not stable adjust trigger level (the pink triangle on right screen border) so as you get a stable display.
- Turn C4 (capacitor trimmer) with a small screw driver so that the waveform displays sharp rightangle (photo C).
- Set [SEN1] switch to 1V and [SEN2] switch to X1 while keep all other settings unchanged. Adjust C6 so that sharp rightangle waveform is displayed.



A - Not enough



B - Too much



C - Good

## Operations

- [SEL] button:** Select parameter to be adjusted. The selected parameter will be highlighted.
- [+] and [-] button:** Adjust the parameter selected by [SEL] button.
- [OK] button:** Freeze waveform refresh (entering HOLD state). Press on it again will de-freeze.
- [CPL] switch:** Set couple to DC, AC, or GND. When GND is selected the scope input is disconnected from outside and connected to ground internally (0V input).
- [SEN1]/[SEN2] :** Adjust sensitivity. The product of [SEN1] and [SEN2] settings makes the actual sensitivity which is displayed at the screen lower-left corner.

## More Operations

Functions	Operations
<b>VPos Alignment</b>	Move cursor to VPos indicator. Hold down [OK] for 3 seconds. Then follow screen prompts.
<b>Measurements ON/OFF</b>	Move cursor to timebase. Hold down [OK] button for 3 seconds to turn ON or OFF on-screen measurements including Vmax, Vmin, Vavr, Vpp, Vrms, Freq., Cycle, Pulse width, and Duty cycle.
<b>Save Waveform</b>	Press [SEL] & [+] buttons simultaneously. The currently displayed waveform will be saved to EEPROM. The existing data in EEPROM will be over-written.
<b>Recall Waveform</b>	Press [SEL] & [-] buttons simultaneously. Recalled waveform is always displayed in Hold state.
<b>Default Restore</b>	Hold down [+] and [-] buttons simultaneously for about 3 seconds.
<b>Center HPos</b>	Move cursor to the top bar. Hold down [OK] button for about 3 seconds. This will move the display window to the center of capture buffer.
<b>Center Trigger Level</b>	Move cursor to trigger level indicator. Hold down [OK] for 3 seconds. This will set the trigger level to the medium value of signal amplitude.
<b>Send Waveform Data</b>	Hold down [SEL] button for 3 seconds will send waveform data in texts via serial port J5. The baudrate is 115200. Data format is 8N1.
<b>Toggle Test Signal Amplitude</b>	Move cursor to trigger slope indicator. Hold down [OK] button for 3 seconds to toggle test signal amplitude between 3.3V and about 0.14V. The amplitude is indicated by $\square$ to screen top.
<b>Calibrate Analog Gain</b>	Move cursor to trigger source indicator. Hold down [OK] button for 3 seconds to enter analog gain calibration mode. Follow the on-screen instructions.